

ABSTRACT:

An electronic device has a data communication bus (200) mounted on a semiconductor substrate (120). The data communication bus (200) has a first conductor (102), a second conductor (104), a third conductor (106) and a fourth conductor (108). The conductors have been reordered and the distances (l_1 , l_2 , l_3) between two neighboring
5 conductors have been recalculated on the basis of the correlation between the data-bits conveyed by the conductors of the data communication bus (200), e.g. the number of times that the two transitions on two conductors have a predetermined value out of the total number of transitions on that conductor pair. Consequently, a data communication bus (200) is
10 obtained in which the power consumption resulting from the charging of the cross-coupling capacitance between two neighboring conductors is reduced.

Fig. 2